

**ABSTRACT OF THE DISCLOSURE:**

5       The semiconductor device includes a first current mirror circuit  
combining analog power sources and digital power sources to receive small  
amplitude signals and constant-voltage input signals, a second current  
mirror circuit for receiving a signal output from the first current mirror circuit  
and for level-converting the signal from analog power source to digital power  
source, a first node provided in the first current mirror circuit, a second node  
10       provided in the second current mirror circuit, and an inverter circuit for  
receiving a signal output on the basis of the voltage levels of the first node  
and the second node and for outputting a CMOS level signal.